

31991 U.S. Pro
10/007836

830



U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NO: 10007836	FILING DATE 11/05/2001	CLASS 712	SUBCLASS	GAU 2154	EXAMINER FOLLANSBEE, J
**APPLICANTS: Moyer William; Arends John; Scott Jeffrey; <i>W. Leg, Pwiz</i>					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/09,260 06/30/2000 PAT 6,327,647					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO		
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			SC90674A CD1		
Verified and Acknowledged Examiners's initials					
TITLE : Method and apparatus for interfacing a processor to a coprocessor					
U.S. DEPT. OF COMM./PAT. & TM-PTO-331 (Rev. 12-94)					

BEST AVAILABLE COPY

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)